Integral University, Lucknow

Deapartment of Electronics & Communication Engineering

B.Tech Improvement Test Schedule 2018-19 (Even Semester)

Date	Day	Time	2 nd Year/4 th Semester	3 rd Year/6 th Semester
April26,2019	Friday	9:15 am-10:45 am	Programming Concepts (EC-208)	Digital Communication & Networks (EC-312)
April26,2019	Friday	11:00 am-12:30 pm	Digital Electronics (EC-209)	Digital Signal Processing (EC-313
April26,2019	Friday	1:30 pm-3:00 pm	Signal and Systems (EC-210)	Microprocessor and Applications (EC-314))
April26,2019	Friday	3:15 pm-4:45 pm	Measurement and Instrumentation (EC-211)	Departmental Elective-I Embedded System Design(EC-316)/Fitter Design(EC-318)
April27,2019	Saturday	9:15 am-10:45 am	Management Concepts In Engg.(BM-227)	Departmental Elective-II RF & Microwave Engg.(EC-319)/ Microwave Devices And Communication(EC-320)
April27,2019	Saturday	11:00 am-12:30 pm	Semiconductor Material and Power Devices(EC-212)	Open Elective-I Solar Engg.(EC-339)/ Wind Engg (EC-340)/ Autotronics (EC-341) (As per subject chosen as open elective)

(Prof. & Head, Dept. of ECE)

Integral University, Lucknow Deapartment of Electronics & Communication Engineering B.Tech Improvement Test Schedule 2018-19 (Even Semester)

Date	Day	Time	1 st Year/2 nd Semester	4 th Year/8 th Semester (NON CBCS)	
April26,2019	Friday	9:15 am-10:45 am	Mathematics-II (MT-112)	Satellite & Radar Communication (IEC-801)	
April26,2019	Friday	11:00 am-12:30 pm	Environmental Studies	Data Communication Network (IEC-802)	
			(ES-101)		
April26,2019	Friday	1:30 pm-3:00 pm	Basic Mechanical Engg.(ME-	Solar Engg. & Enterprise (EC-803)	
			101)		
April26,2019	Friday	3:15 pm-4:45 pm		Consumer Electronics (IEC-031)	
April27,2019	Saturday	9:15 am-10:45 am	Computer Programming	Information Theory & Coding (IEC-041)	
			(CS-101)		
April27,2019	Saturday	11:00 am-12:30 pm	Chemistry	Solar Engg(EC-339)/Wind Engg (EC-	
			(CH-101)	340)/Autotronics (EC-341)	
				Open Elective (for CBCS system only)	
				(As per subject chosen as open elective)	

(Prof. & Head, Dept. of ECE)

Integral University, Lucknow

Deapartment of Electronics & Communication Engineering

M.Tech. Improvement Test Schedule 2018-19 (Even Semester)

Date	Day	1st shift	2nd Shift	3rd Shift	4th Shift
		9:15 am to 10:45 am	11:00am to 12:30	1:30 pm to 3:00 pm	3:15 pm to 4:45 pm
			pm		
26/04/2019	Friday	VLSI Design	Analog MOS Circuits		
		(EC- 506)	(EC -507)		
27/04/2019	Saturday	ASIC Design & FPGA(EC -508)	Fault Modelling& Testing of Electronic Circuits(EC -509)		

(Prof. & Head, Dept. of ECE)